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The 8086 (["eighty eighty-six"], also called iAPX 86) is a 16-bit microprocessor chip designed by Intel between early 1976 and mid-1978, when it was released.

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Summary of Contents for Intel 8086

Page 1 Arithmetic in Binary or Decimal  
Including Multiply and Divide The Intel  
8086 high performance 16-bit CPU is  
available in three clock rates 5 8 and  
10 MHz The CPU is implemented in N-  
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The Intel 8086 high performance 16-bit CPU is available in three clock rates: 5, 8 and 10 MHz. The CPU is implemented in N-Channel, depletion

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load, silicon gate technology (HMOS-III), and packaged in a 40-pin CERDIP or plastic package. The 8086 operates in both single processor and multiple processor configurations to achieve high performance levels.

~~8086 16-BIT HMOS~~

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Definition: 8086 is a 16-bit microprocessor and was designed in 1978 by Intel. Unlike, 8085, an 8086 microprocessor has 20-bit address

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bus. Thus, is able to access 2<sup>20</sup> i.e., 1 MB address in the memory. As we know that a microprocessor performs arithmetic and logic operations.

~~8086 Microprocessor~~

8086 Microprocessor Cheatsheet A  
short cheatsheet of 8086

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Microprocessor including overview of 8086, internal architecture of 8086, addressing modes, register organization, modes of operations, interrupts, and Diagrams. Program bank and Question bank section is also included for completeness. (Let me know what are your suggestions).

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8086 Microprocessor is an enhanced version of 8085 Microprocessor that was designed by Intel in 1976. It is a 16-bit Microprocessor having 20 address lines and 16 data lines that provides up to 1 MB storage. It consists

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of powerful instruction set, which provides operations like multiplication and division easily.

## ~~Features of 8086~~

The 8086 microprocessor supports 8 types of instructions □ Data Transfer Instructions; Arithmetic Instructions; Bit

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Manipulation Instructions; String  
Instructions; Program Execution  
Transfer Instructions (Branch & Loop  
Instructions) Processor Control  
Instructions; Iteration Control  
Instructions; Interrupt Instructions; Let  
us now discuss these instruction sets  
in detail. Data Transfer ...



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~~Microprocessor 8086 Instruction Sets~~

The 8086 microprocessor is a 16-bit, N-channel, HMOS microprocessor.

Where the HMOS is used for " High-speed Metal Oxide Semiconductor ".

Intel 8086 is built on a single semiconductor chip and packaged in a

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40-pin IC package. The type of package is DIP (Dual Inline Package).

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Definition: 8086 is a 16-bit  
microprocessor and was designed in  
1978 by Intel. Unlike, 8085, an 8086

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Microprocessor has 20-bit address bus. Thus, is able to access  $2^{20}$  i.e., 1 MB address in the memory..

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8086 Microprocessor is divided into two functional units, i.e., EU

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(Execution Unit) and BIU (Bus Interface Unit). EU (Execution Unit) Execution unit gives instructions to BIU stating from where to fetch the data and then decode and execute those instructions. Its function is to control operations on data using the instruction decoder & ALU. EU has no

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direct connection with system buses  
as ...

~~Microprocessor 8086 Functional  
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□ When a CALL is executed, the 8086 automatically PUSHes the current value of CS and IP onto the stack.

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- Other registers can also be pushed
- Before return from the subroutine, POP instructions can be used to pop values back from the stack into the corresponding registers.

Prof.Ms.Aaradhana A. Deshmukh,  
SKNCOE, Comp

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The 8086 provides many arithmetic

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Operations: addition, subtraction,  
negation, multiplication and comparing  
two values.

~~Instruction set and assembler  
directives of 8086 ...~~

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The Intel M80C86 is a high performance, CHMOS version of the industry standard HMOS M8086 16-bit CPU. It is available in 5 and 8 MHz

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clock rates. The M80C86 offers two modes of operation: MINimum for small systems and MAXimum for larger applications such as multiprocessing. It is available in 40-pin DIP package.

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