

Gps Receiver Module Manual

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Gps Receiver Module Manual The GPS Receiver Module is controlled by the host via a standard TTL-level, asynchronous serial communications interface. A single pin (SIO) transfers commands sent TO the Module and data received FROM the Module. All communication is at 4800 bps, 8 data bits, no parity, 1 stop bit, non-inverted. Parallax GPS Receiver Module (#28146) View and Download Navman LA000508 ...

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The GPS module A2200-A is Maestro's first implementation of CSR's SiRFstarIV ROM chip on GPS module. This is highly integrated GPS receivers that can be used as SMT components. A very easy implementation (power, serial, ON_OFF, and antenna) allows receiving position, velocity and time information.

GPS Receivers A2200-A - Richardson RFPD
V1.5 - Oct-13 User's Manual Page 6 of 39 1 Introduction The GPS module A2200-A is Maestro's first implementation of CSR's SiRFstarIV ROM chip on GPS module. This is highly integrated GPS receivers that can be used as SMT components. A very easy implementation (power, serial, ON_OFF, and antenna) allows receiving position, velocity and time information. The A2200-A is a module designed ...

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Pharos iGPS-360 - GPS receiver module Specs - CNET
This GPS receiver module kit includes an extremely sensitive patch antenna, with factory pre-soldered Mediatek chipset RF module. It has three onboard LEDs for visual status indication, 3.3V regulator and power supply filtering, and 5V logic level conversion. It's easy to build and directly compatible with all QRP Labs kits.

GPS receiver kit QLG1 - QRP Labs
The R4 Series GPS receiver module is a self-contained high-performance GPS receiver. Based on the SiRFstar IV chipset, it provides exceptional sensitivity, even in dense foliage and urban canyons. The module's very low power consumption helps maximize runtimes in battery powered applications.

R4 Series GPS Receiver Module Data Guide
The 6-way set of connections provides access to the full set of signals, for general purpose use of this GPS receiver module. Note that TXD is the serial data output of the module, at 5V Logic levels. The 1 pulse per second (1pps) timing signal is also 5V.

QLG1 GPS Receiver kit - QRP Labs
This manual applies to all ANTARIS#4 GPS receivers based on Firmware 5.00 and above. u-blox AG Z\u00fcrcherstrasse 68 8800 Thalwil Switzerland www.u-blox.com Phone +41 44 722 7444 Fax +41 44 722 7447 info@u-blox.com ANTARIS#4 GPS Modules System Integration Manual (SIM) (incl. Reference Design) Manual This modul These mo co excell GPS pe your position is our focus Title GPS Modules Subtitle ...

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